# **CSP**<sup>++</sup> : Software Synthesis from Formal Specifications

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*Tool available for free download:* http://www.uoguelph.ca/~gardnerw/csp++/

# **Background**

#### formal methods:

- using mathematically-based languages and techniques to specify, model, implement, and verify systems
- Advantage: code is provably correct
- *Disadvantage:* perceived difficulty and cost; hard to find enough experts

### CSP:

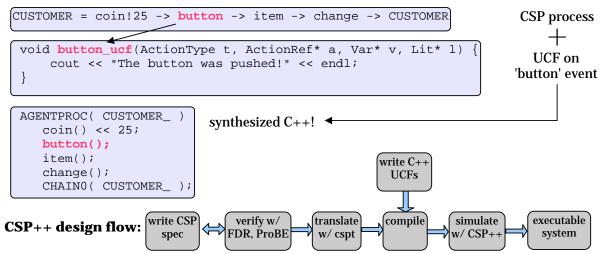
- Communicating Sequential Processes (Hoare)
- process-oriented, event-based formal algebra
- process defined as *sequence* of abstract events and/or hierarchical *composition* (parallel or sequential) of other processes
- interprocess communication via message passing on channels (nonbuffered, unidirectional)
- synchronization occurs on events and channel I/O
- verify formal properties (deadlock, livelock, refinement) via third-party tools
- Timed CSP adds operators for delay, interrupt, timeout, suitable for soft real-time systems

# <u>CSP++</u>

## what it is:

- object-oriented application framework (OOAF) plus translator
- classes implement semantics of CSP primitives-processes, events, and channels
- **cspt** translator synthesizes C++ from CSP spec for compilation with OOAF library
- developers supply C++ user-coded functions (UCFs) to overload events in CSP control backbone
- latest version supports Timed CSP operators
- compatible with \*NIX platforms with GNU g++ and Pth (portable threading library)

#### example:



FDR and ProBE are verification tools from Formal Systems (Europe) Ltd.

### selective formalism:

- a compromise between full-formal and nonformal development
- only apply formal methods where most beneficial: modelling concurrency, common source of problems (e.g., deadlocks)
- SW synthesis eliminates manual translation from formal specs to source code
- minimal gurus required; ordinary C++ developers still useful!